	Application No.	Applicant(s)	
Notice of Allowability	10/064,462	KUNG ET AL.	
	Examiner	Art Unit	
	Terri L. Smith	3762	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to the Amendment filed on 09 September 2005.			
2. The allowed claim(s) is/are 14-17.			
3.	been received. been received in Application No cuments have been received in this rec	national stage applical complying with the red S AMENDMENT or Nation is deficient.	quirements
(b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the state of the	.84(c)) should be written on the drawin	gs in the front (not the	e back) of
DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendn 8. Examiner's Stateme 9. Other	(PTO-413), e nent/Comment	,

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REASONS FOR ALLOWANCE

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1. The following is an Examiner's statement of reasons for allowance: The prior art does not teach all of the limitations of the claimed invention, including at least one strip of solder between a major surface of an IC package and a major surface of a circuit board to mechanically attach an IC package to a circuit board without providing an electrical contact between the IC package and the circuit board.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the 2. Examiner should be directed to Terri L. Smith whose telephone number is 571-272-7146. The Examiner can normally be reached on Monday - Friday, between 7:30 a.m. - 4:00 p.m..

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Angela Sykes can be reached on 571-272-4955. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, gontact the Electronic Business Center (EBC) at 866-217-9197 (toll-free)

September 26, 2005
No September 2005